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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Tsuyoshi YAMAMOTO et al.**

Group Art Unit: **3729**

Serial No.: **10/073,106**

Examiner: **Paul D. Kim**

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For: **METHOD OF MOUNTING ELECTRONIC COMPONENT ON
SUBSTRATE WITHOUT GENERATION OF VOIDS IN BONDING
MATERIAL**

Attorney Docket Number: **020154**
Customer Number: **38834**

RESPONSE TO PATENT OFFICE COMMUNICATION

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: May 19, 2005

Sir:

This paper is filed in response to the Patent Office Communication dated May 13, 2005.

Remarks being on page 2 of this paper.